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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/904,334	07/12/2001	Paul McAlinden	ITL.0609US (P11750)	1583
21906 TROP PRUNE	7590 08/24/2007 R & HII PC	EXAMINER		
1616 S. VOSS	ROAD, SUITE 750		ADDY, THJUAN KNOWLIN	
HOUSTON, TX 77057-2631			ART UNIT	PAPER NUMBER
			2614	
	•		MAIL DATE	DELIVERY MODE
			08/24/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)			
	09/904,334	MCALINDEN, PAUL			
Office Action Summary	Examiner	Art Unit			
	Thjuan K. Addy	2614			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the	correspondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory period w Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be to the second will expire SIX (6) MONTHS from the second ABANDON cause the application to become ABANDON	DN. timely filed m the mailing date of this communication. IED (35 U.S.C. § 133).			
Status ,					
Responsive to communication(s) filed on <u>31 M</u> . This action is FINAL . 2b)⊠ This Since this application is in condition for allowar closed in accordance with the practice under E	action is non-final. nce except for formal matters, p				
Disposition of Claims					
4) □ Claim(s) 1-5 and 7-30 is/are pending in the approach 4a) Of the above claim(s) is/are withdraw 5) □ Claim(s) is/are allowed. 6) □ Claim(s) 1-5 and 7-30 is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and/or	vn from consideration.				
Application Papers	•				
9)☐ The specification is objected to by the Examine 10)☒ The drawing(s) filed on 12 July 2001 is/are: a)[Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11)☐ The oath or declaration is objected to by the Ex	☑ accepted or b) ☐ objected to drawing(s) be held in abeyance. So ion is required if the drawing(s) is o	ee 37 CFR 1.85(a). bjected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.					
Attachment(s)					
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summal Paper No(s)/Mail (5) Notice of Informal 6) Other:				

DETAILED ACTION

Response to Amendment

1. Applicant's amendment filed on May 31, 2007 has been entered. Claims 1, 11, and 21 have been amended. Claim 6 has been cancelled. No claims have been added. Claims 1-5 and 7-30 are now pending in this application, with claims 1, 11, and 21 being independent.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-5, 8-13, 15-23, and 25-30 are rejected under 35 U.S.C. 103(a) as being anticipated by Nagasawa (US 6,782,281), in view of Beck et al (US 2001/0014097 A1).
- 3. In regards to claims 1, 12, 13, 15, 16, 22, 23, 25, and 26 Nagasawa discloses a cellular telephone (e.g., folding portable telephone apparatus, See Fig. 6), method, and article comprising: a first processor (See Fig. 6 and controller for pocket game 28); a second processor (See Fig. 6 and controller 22); a first bus coupling a first bus (See Fig. 6 and link/bus connecting the controller for pocket game 28 to the controller 22) coupling said first and second processors. Nagasawa, however, does not disclose a device to selectively bypass the first processor, if the first processor fails to respond, by

diverting signals from the first to the second processor. Beck, however, does disclose a device to selectively bypass (for example, redirect from) the first processor, if the first processor fails to respond, by diverting signals from the first to the second processor (See pg. 7, paragraph [0075], pg. 8, paragraph [0078], and Claim 13). Therefore, it would have been obvious for one of ordinary skill in the art at the time of the invention to incorporate this feature within the system, as a way of preventing signals directed to a first processor from being dropped during a routing failover delay, thus providing backup in case failure occurs in the process, thus allowing the user to maintain connection, and continue or establish communication.

- 4. In regards to claims 2 and 19, Nagaswa discloses the telephone and method, wherein said first processor is an applications processor (See Fig. 6 and controller for pocket game 28, for example, the controller for pocket game 28 allows the user to play and/or access games, which are considered forms of applications/programs, See col. 5 lines 17-22).
- 5. In regards to claim 3, Nagasawa discloses the telephone, including a keypad (See Fig. 6 and key operation area 26), said first processor coupled to said keypad to receive keypad inputs (See col. 5 lines 10-14).
- 6. In regards to claim 4, Nagasawa discloses the telephone, including a display (See Fig. 6, first display 4, and second display 8), said first processor coupled to said display to provide outputs to said display (See col. 4-5 lines 66-3).
- 7. In regard to claim 5, Nagasawa discloses the telephone, wherein said second processor is a baseband processor (See Fig. 6 and controller 22, for example, controller

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22 performs the same functions as that of a baseband processor (See col. 5 lines 4-14).

- 8. In regard to claim 8, Nagasawa discloses the telephone, wherein said telephone includes a keypad (e.g., key operation area 26), keypad entries being provided to said first processor (e.g., controller for pocket game 28), said device (e.g., call release button 5b) selectively shunting (for example, turning off to one side or going around) said keypad entries to said second processor (e.g., controller 22) (See col. 5 lines 17-22 and col. 6 lines 21-25).
- 9. In regards to claim 9, Nagasawa discloses the telephone, including a display, said display coupled to receive outputs (e.g., games) from said first processor, said device (e.g., call start button 5a) to selectively bypass (e.g., suspend) the first processor to provide outputs (e.g., caller information, i.e., the telephone number of the caller or caller's name) to said display (e.g., first display 4) from said second processor (e.g., controller 22) (See col. 6 lines 13-20).
- 10. In regards to claim 10, Nagasawa discloses the telephone, including a display (e.g., first display 4 and second display 8) and a keypad (e.g., key operation area 26), said first processor (e.g., controller for pocket game 28) coupled to said display and said keypad and said second processor (e.g., controller 22) coupled to said display and said keypad through said first processor and said device (See Fig. 6 and col. 6 lines 13-35).
- 11. In regards to claims 11 and 21, Nagasawa discloses a method and article comprising: establishing communications between an input/output device (e.g., input/key operation area 26 and output/first display 4) and a first processor (See Fig. 6 and controller for pocket game 28) to execute a first task (for example, the first task may

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simply be the start of a game). Nagasawa, however, does not disclose in response to the detection of an event, providing said communications to a second processor so that the second processor executes the first task in place of the first processor. Beck, however, does disclose in response to the detection of an event (e.g., failover delay), providing said communications to a second processor so that the second processor executes the first task (e.g., transmission of data packets) in place of the first processor (See pg. 7, paragraph [0075], pg. 8, paragraph [0078], and Claim 13).

- 12. In regards to claim 17, Nagasawa discloses the method, including coupling said second processor (e.g., controller 22) to said first processor (e.g., controller for pocket game 28) and coupling said first processor directly to a keypad (e.g., key operation area 26) and a display (e.g., first display 4) (See Fig. 6).
- 13. In regards to claims 18 and 28, Nagasawa discloses the method and article, including selectively coupling (i.e., connecting) said display (e.g., first display 4 and second display 8) and said keypad (e.g., key operation area 26) directly to said second processor (e.g., controller 22) (See Fig. 6 and col. 6 lines 13-20).
- 14. In regards to claims 20 and 30, Nagasawa discloses the method and article, including providing a second processor that acts as a baseband processor (See Fig. 6 and controller 22, for example, controller 22 performs the same functions as that of a baseband processor) (See col. 5 lines 4-14).
- 15. In regards to claim 27, Nagasawa discloses the article, further storing instructions that enable the processor-based system to couple said second processor (e.g., controller 22) to said first processor (e.g., controller for pocket game 28) and couple

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said first processor directly to a keypad (e.g., key operation area 26) and a display (e.g., first display 4) (See Fig. 6, col. 5 lines 17-22, and col. 6 lines 13-17)..

- 16. In regard to claim 29, Nagasawa discloses the article, further storing instructions that enable the processor-based system to establish communications (e.g., games) between an input/output device (e.g., input/key operation area 26 and output/first display 4) and a first processor (e.g., controller for pocket game 28) that is an applications processor (See col. 5 lines 17-22).
- 17. Claims 7, 14, and 24 are rejected under 25 U.S.C. 103(a) as being unpatentable over Nagasawa (US 6,782,2810, in view of Beck et al (US 2001/0014097 A1), and further in view of Koenck et al (US 6,014,705).
- 18. In regards to claims 7, 14, and 24, Nagasawa and Beck disclose all of claims 7, 14, and 24 limitations, except wherein the second processor selectively bypasses the first processor to make an emergency call. Koenck, however, does disclose the second processor (See Fig. 2 and control processor 49) being selectively bypassed in the even of a power failure, battery low indication, or other "event" (e.g., emergency) (See col. 20 lines 53-64, col. 26-27 lines 35-4, and col. 27 lines 5-18). Therefore, it would have been obvious for one of ordinary skill in the art at the time of the invention to incorporate this feature within the system, as a way of making an emergency call even when a processor is handling a particular task or receiving a signal.

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Response to Arguments

19. Applicant's arguments with respect to claim 1-5 and 7-30 have been considered but are most in view of the new ground(s) of rejection.

Conclusion

- 20. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Beck et al (US Patent Application, Pub. No.: US 2004/0073683 A1) teach a method and apparatus for providing an integrated cluster alias address.
- 21. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thjuan K. Addy whose telephone number is (571) 272-7486. The examiner can normally be reached on Mon-Fri 8:30-5:00pm.
- 22. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ahmad Matar can be reached on (571) 272-7488. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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23. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business

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800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Thjuan K. Addy Patent Examiner

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